

REMARKS:

The specification and claims of the referenced application have been amended in accordance with common U.S. Patent Practice. Claims 1-8 were canceled. New Claims 9-16 were added. No new matter has been introduced through the foregoing amendments. Entry is in order.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 07-1337 and please credit any excess fees to such deposit account.

Respectfully submitted,

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**DEVICE FOR NON-DISSIPATIVE MEASUREMENT OF THE CURRENT
IN AN INDUCTOR**

Field of the invention :

- 5 The invention relates to an inexpensive and compact non-dissipative measurement device for the current flowing through an inductor inserted into an electrical or electronic device.

Background of the invention :

- 10 In certain electronic applications it is necessary to measure the value of the current in the inductors with good accuracy and while disturbing as little as possible the device in which the inductor is inserted. We may cite for example the measurement of the current in the inductors of switched mode power supplies, among other things, voltage step-down or step-up
15 switched mode serial choppers (respectively known as "buck converters" and "boost converters").

The conventional method which is most used consists in placing a measurement shunt in series with the inductor, thereby making it possible
20 to obtain the image of the current in the inductor by measuring the voltage across the terminals of the shunt. This solution has the advantage of being simple to implement, on the other hand it has a major drawback. Specifically, the power dissipated in the shunt contributes to a degradation in the overall efficiency of the converter, which is not always acceptable,
25 this being all the more true when the output voltage of the energy conversion device is low. To avoid this constraint, a non-dissipative solution for measuring current, presented in figure 1, is used.

Figure 1 shows a diagram of a device 10 for measuring the current I
30 passing through an inductor 12. The inductor 12 is represented by its equivalent diagram comprising a pure reactive part, i.e. the inductive part L in series with a resistor R_L . The inductor comprises a terminal A and a terminal B.

- 35 The inexpensive and very non-dissipative measurement device 10 is placed in parallel with the terminals A and B of the inductor 12. The measurement device 10 comprises a resistor R2 in series with a resistor R1 in parallel with a capacitor C1, the resistor R2 being connected to the terminal A, the resistor R1 to the terminal B.

In the device for measuring the current of figure 2, the voltage which is the image of the current in the inductor is no longer V_{C1} , as in the device of figure 1, but becomes the voltage V_{mes} .

- 5 The measurement circuit of figure 2 furthermore comprises elements of figure 1, an offset circuit 14 having a DC voltage V_{out} generator E connected in parallel, with an offset resistor R_{offset} in series with two resistors in parallel $R3$ and $R4$. The positive pole of the generator being connected to the common point of the two resistors $R3$ and $R4$ and to the
- 10 terminal B of the inductor 12, the negative pole of the generator being connected to the resistor R_{offset} . The voltage generator E may be the output capacitor of a converter, this being the case for a Buck type chopper, for example.
- 15 It is also possible to place a capacitor $C1'$ in parallel with $R3$ and $R4$ so as to balance the impedances on the two branches of the measurement device.

20 The voltage V_{mes} is measured between the common point ca between the two resistors $R2$ and $R1$ and the common point cb between the resistor R_{offset} and the two resistors $R3$ and $R4$ in parallel.

The expression for V_{mes} relating to the device of figure 2 may be written:

25
$$V_{mes} = \left(\frac{R1 \cdot RL}{R1 + R2} \right) \cdot I + V_{out} \cdot \left(1 - \frac{R_{offset}}{\frac{R3 \cdot R4}{R3 + R4} + R_{offset}} \right) \quad \text{equation (3)}$$

The voltage V_{out} being constant, the voltage V_{offset} is therefore also constant, the equation obtained is therefore of the form: $V_{mes}(I) = a \cdot I + b$, with:

30
$$a = \left(\frac{R1 \cdot RL}{R1 + R2} \right) \quad \text{and} \quad b = V_{out} \cdot \left(1 - \frac{R_{offset}}{\frac{R3 \cdot R4}{R3 + R4} + R_{offset}} \right)$$

This type of measurement is used in devices such as the switched mode converters of power electronics, for which it is necessary to limit the current

The aim of this arrangement of the state of the art is to obtain a voltage across the terminals of the capacitor C1 proportional to the voltage across the terminals of the resistor RL of the inductor 12 hence proportional to the current I in the inductor 12 (or in the inductor L). The currents in the resistors R2 and R1 are negligible compared with the current I in the inductor 12.

To size the elements of the device, it is important to comply with the following constraint: $\frac{L}{RL} = \frac{R1 \cdot R2}{R1 + R2} \times C1$ equation (1)

If the condition expressed by equation (1) is satisfied, V_{C1} is the image of the current in the inductor. The voltage V_{C1} across the terminals of C1 is given by the following relation:

$$V_{C1} = \left(\frac{R1 \cdot RL}{R1 + R2} \right) \cdot I \quad \text{equation (2)}$$

The device of figure 1 therefore makes it possible to obtain the image of a current passing through an inductor on condition that the value of the intrinsic resistance RL of the inductor is known.

It may be pointed out that the image of the current is given by an equation of the type: $V_{C1}(I) = a \cdot I$ with "a" the proportionality coefficient.

For certain applications, it is necessary that the measurement exhibit an offset voltage, that is to say that contrary to the above equation (2), when the current I is zero the voltage V_{C1} is not zero. The equation which conveys this behavior is of the form $V_{C1}(I) = a \cdot I + b$ with: "a" the proportionality coefficient and "b" the ordinate at the origin (offset voltage in our case).

To produce this offset in the measurement, it suffices to join an additional arrangement to the device of figure 1.

Figure 2 shows a device for measuring the current I in the inductor 12 with a voltage offset V_{offset} .

passing through the inductors. For this purpose, the measurement voltage V_{mes} is compared, with the aid of a threshold-based comparator, with a threshold voltage $V_{threshold}$ corresponding to a maximum current I_{max} .

5 However the device of the prior art represented in figure 2, has a major drawback since the value of the resistance R_L of the inductor depends on the temperature to which it is subjected, the current measurement which is obtained therefore exhibits an error related to the temperature. The consequence is that the measurement of the current in the inductor and
10 therefore the limitation of current I_{lim} depends on the temperature.

Figure 3 shows a curve of variation of the limitation current I_{lim} as a function of the temperature T of a current limiting circuit comprising a threshold-based comparator and the measurement device of figure 2. The
15 variation in the limitation current as a function of the temperature is 60% between -40°C and 100°C . Such a scatter in the value of the limiting current gives rise to an oversizing of the power circuit so that it can withstand the limiting current at low temperature, this representing a major drawback.

20 Summary of the invention :

In order to alleviate the drawbacks of the devices for measuring current of the prior art, the invention proposes a device for measuring current in an inductor, which device is intended to be connected in parallel with said
25 inductor, comprising two terminals A and B, characterized in that it comprises:

- a network in parallel with the inductor and connected to the terminals A and B having a resistor R_2 in series with a resistor R_1 in parallel with a capacitor C_1 ;
- 30 - a voltage offset circuit having a DC voltage generator E connected in parallel with an offset resistor (R_{offset}) in series with two resistors in parallel R_3 and R_4 , the positive pole of the generator being connected to the common point of the two resistors R_3 and R_4 and to the common point of the resistor R_1 and the capacitor C_1 of the network intended to
35 be connected to one of the two terminals of the inductor, the negative pole of the generator E being connected to the offset resistor;
- a temperature compensation circuit comprising a current source controlled as a function of the temperature, one of the two terminals of the current source being connected to the negative pole of the generator

E, the other terminal of the current source being connected to different points of the measurement device according to the direction of variation of the current of the source as a function of the temperature; and in that the measurement of voltage V_{mes} , the image of the current in the inductor, is performed between the common point of the resistors R_1 , R_2 of the network and the common point of the offset resistor and of the two resistors R_3 and R_4 .

It is moreover necessary to satisfy the condition expressed by the following equation:

$$\frac{L}{R_L} = \frac{R_1 \times R_2}{R_1 + R_2} \times C_1,$$

considering the inductor as represented by an equivalent diagram comprising an inductive part L and a resistor part R_L in series, so that the voltage V_{C1} across the terminals of the capacitor C_1 is independent of the voltage V_L across the terminals of the inductor.

In an embodiment of the device for measuring current according to the invention, the variation in current of the current source as a function of the temperature being effected in a first direction, the other terminal of the current source is connected to the point of connection between the resistors R_1 and R_2 of the network.

In another embodiment of said device, the variation in current of the current source as a function of the temperature being effected in a second direction, the other terminal of the current source is connected to the point of connection between the resistors R_3 , R_4 and the offset resistor (R_{offset}) of the compensation circuit.

Figures 4b, 4c and 4d show various representations of the current source of the temperature compensation circuit.

The current source controlled as a function of temperature θ may be represented by:

- an ideal model of current source $I_{perf}(\theta)$, represented in figure 4b;
- an equivalent Norton model $I_{nor}(\theta)$, represented in figure 4c, comprising a perfect current source varying as a function of temperature in parallel

- with a resistor R_{nor} ;
- an equivalent Thevenin model, represented in figure 4d, comprising a voltage source $E_{th}(\theta)$ varying as a function of temperature in series with a resistor R_{th} .

5

According to the direction of variation of the current of the current source for the temperature compensation, the temperature compensation circuit will be linked by one of these two terminals, either to the common point between the two resistors R_1 and R_2 of the network, or to the point of
10 connection between the resistors R_3 , R_4 and the offset resistor (R_{offset}) of the compensation circuit and by the other terminal to the negative pole of the generator E . In the latter case the temperature compensation circuit is in parallel with the offset resistor (R_{offset}).

15 This device makes it possible, by injection of current controlled as a function of temperature, to obtain a voltage (V_{mes}) which is the image of the current in the inductor regardless of the temperature of the latter.

The current source of the temperature compensation circuit may be
20 embodied in various ways, for example on the basis of voltages across the terminals of networks of electrical or electronic components among which we may cite:

- temperature dependent variable resistors (platinum resistors, silicon resistor, for example resistor known commercially as KTY from the
25 manufacturer "INFINEON" ...);
- diodes whose threshold voltage varies with temperature ($-2 \text{ mV}/^\circ\text{C}$);
- or any other electronic circuit providing a temperature dependent voltage or current (integrated temperature sensor such as the AD590 from the manufacturer "Analog Device" ...).

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A capacitor C_1' may be added in parallel with R_3 and R_4 so as to improve the operation of the device and increase its precision.

Brief description of drawings :

35 The invention will be better understood with the aid of a nonlimiting exemplary embodiment of a measurement device according to the invention with reference to the figures in which:

- figure 1, already described, represents a device for measurement of the state of the art of the current passing through an inductor;

- figure 2, already described, represents a device for measuring the current in the inductor of figure 1, with a voltage offset;
- figure 3, already described, represents a curve of variation of the limiting current as a function of the temperature of a current limiting circuit using the measurement device of figure 2;
- figure 4a represents a device for measuring current in an inductor in a general case of the invention with a voltage offset;
- figures 4b, 4c and 4d already described show various representations of the current source of the temperature compensation circuit;
- figure 5 shows an embodiment by way of example of the device for measuring current in an inductor, according to the invention;
- figure 6 shows by way of comparison the variations in the limiting current as a function of temperature with the device according to the invention (continuous plot) and without the device (discontinuous plot).

Detailed description of embodiments :

The diagram of figure 4a shows a device for measuring current in an inductor in a general case of the invention with a voltage offset.

Figure 4a shows:

- the inductor 12 represented by its equivalent diagram comprising a pure reactive part L, in series with a resistor RL;
- a device for measuring current in the inductor 12, according to the invention, connected in parallel with the inductor comprising the two terminals A and B.

The device of figure 4a comprises:

- the network 10 as represented in figure 2 (or the measurement device of the state of the art of figure 1) in parallel with the inductor 12 and connected to the terminals A and B having the resistor R2 in series with the resistor R1 in parallel with the capacitor C1;
- a voltage offset circuit 16 having the generator E of DC voltage Vout connected in parallel with the offset resistor Roffset in series with the two resistors in parallel R3 and R4 and to the common point of the resistor R1 and the capacitor C1 linked to the terminal B of the inductor 12. The negative pole of the generator is connected to the offset resistor Roffset;
- a temperature compensation circuit 20 comprising a current source varying as a function of temperature possibly being represented by a perfect current source varying as a function of temperature $I_{perf}(\theta)$

(figure 4b) or by an equivalent Norton model $I_{nor}(\theta)$ in parallel with a resistor R_{nor} (figure 4c), or an equivalent Thevenin model (figure 4d) comprising a voltage source varying as a function of temperature $E_{th}(\theta)$ in series with a resistor R_{th} .

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According to the direction of variation of the current of the temperature compensation source (current generator), the temperature compensation circuit will be connected either, for a first direction of variation of the current, by one of these two terminals, to the common point between the two resistors R_1 and R_2 of the network 10 by the connection S_1 represented as a discontinuous line in figure 4a, or, for the second direction of variation of current, connected to the common point of the offset resistor and of the two resistors R_3 and R_4 by the connection S_2 represented as a discontinuous line (i.e. in this second case in parallel with the offset resistor R_{offset}).

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The capacitor C_1' may be added in parallel with the resistors R_3 and R_4 so as to improve the operation of the device and increase its precision.

20 The voltage measurement V_{mes} which is the image of the current I in the inductor 12 is performed between the common point between the resistors R_1 , R_2 of the network and the common point between the offset resistor R_{offset} and the two resistors R_3 and R_4 .

25 Figure 5 shows a first embodiment of the device for measuring current in an inductor, according to the invention.

The device comprises:

- the network 10 as represented in figure 2 in parallel with the inductor 12 and connected to the terminals A and B having the resistor R_2 in series with the resistor R_1 in parallel with the capacitor C_1 ,
- a voltage offset circuit 22 comprising the generator E connected in parallel with an offset resistor R_{15} in series with two resistors R_3 and R_4 in parallel. The positive pole of the generator E is connected to the common point of the two resistors R_3 and R_4 on the side of these resistors and to the common point of the resistor R_1 and the capacitor C_1 linked to the terminal B of the inductor 12, the negative pole of the generator E being linked to the offset resistor R_{15} ;
- a temperature compensation circuit 24 comprising a resistor R_{14} linked

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by one of these two terminals to the common point between the two resistors R1 and R2 of the network and by the other terminal to an output ps of a divider bridge comprising a resistor R13 in series with a thermistor RKTY (ref.: KTY 13-6 from INFINEON), the divider bridge
5 being connected, on the side of the thermistor RKTY to the positive pole of the generator E and on the side of the resistor R13 to the negative pole of the generator E.

The voltage source E_{th} for temperature compensation (figure 4d) is
10 obtained in the case of the embodiment of figure 5 through the voltage of the common point between the resistors RKTY and R13.

A capacitor C1' is connected in parallel with R3 and R4 so as to improve the operation of the device and increase its precision.

15 The voltage measurement V_{mes} which is the image of the current I in the inductor 12 is performed between the common point of the resistors R15, R3, R4 and the common point of the resistors R14, R1, R2.

20 We shall hereinbelow express the relations existing between the various elements of the device according to the invention represented in figure 5.

The resistance R_L represents the intrinsic resistance of the inductor 12. The resistance R_L varies with temperature and with no compensation
25 circuit an error of measurement of the current in the inductor occurs.

We shall consider, by way of example, the case of the use of the measurement device according to the invention of figure 5 in a switched mode voltage converter operating with a switching frequency F, having a
30 current limiting circuit (not represented in the figures). The limiting circuit essentially comprises a threshold-based comparator receiving on the one hand the voltage V_{mes} which is the image of the current in the inductor 12 and on the other hand a threshold voltage V_{comp} representing a maximum current not to be exceeded. In the exemplary embodiment of figure 5, the
35 values of the components are:
 $R_{L0}=8m\Omega$ for an ambient temperature $T_0=25^\circ C$.

The resistance of the inductor $R_L(T)$ as a function of temperature is given by

$$RL(T)=RL0.[1+0.0038(T-T0)].$$

The diagram of figure 5 allows thermal compensation of the measurement of the current to obtain a maximum current limitation independent of temperature. To this end use is made of an RKTY thermistor based on silicon the resistance of which varies in a substantially linear manner as a function of temperature.

$$RKTY0=2000\Omega,$$

$$RKTY0 \text{ for an ambient temperature of } T0=25^{\circ}\text{C},$$

$$RKTY(T)=RKTY0.[1+7.8810^{-3}.(T-T0)+19.3710^{-6}.(T-T0)^2].$$

The system will be sized between the two extreme temperatures T_h and T_b . This is the temperature of the inductor at full power, therefore comprising the external ambient temperature added to the rise in temperature internal to the box in which the inductor may be located and that of the inductor.

The current measurement V_{mes} is applied to an input of the current comparator circuit (not represented in the figures). The maximum threshold V_{comp} of the current V_{mes} corresponding to a limiting current I_{max} is applied to the other input of the comparator. For example in this embodiment:

$$V_{comp}=75\text{mV}$$

The voltage V_{offset} to be applied to the input of the comparator, when the current is zero, is:

$$V_{offset}=3\text{mV}$$

The maximum output current I_{max} from the converter (mean current in the inductor) is:

$$I_{max}=8\text{A}, \text{ for a nominal current}$$

As was stated previously, to obtain a correct measurement of current in the inductor, the following relation must be complied with:

$$R_{1P2} \cdot C1 = \frac{L}{RL}$$

R_{1P2} being the resistance equivalent to $R1$ and $R2$ in parallel.

As RL varies with temperature, in practise, the mid value RL_{med} may be

chosen.

- 5 R13 will be chosen a priori to obtain a bridge voltage (RKTY, R13) which changes significantly with temperature while limiting the current in RKTY so as to avoid self-heating.

It is therefore possible to express the relation linking Vmes and the current I in the inductor as follows:

10
$$V_{mes} = \frac{R2 \cdot R14}{(R1 + R2) \cdot (R_{1P2} + R14)} \cdot RL \cdot I + V_{out} \cdot \left(\frac{R_{1P2}}{R_{1P2} + R15} - \frac{RKTY}{RKTY + R13} \cdot \frac{R_{1P2}}{R_{1P2} + R14} \right)$$

with RL(T) resistance of the inductor as a function of temperature,
with RKTY(T) temperature dependent variable resistance.

- 15 The calculation of the limiting current Ilim adjusted by the threshold voltage on the comparator is given by the expression:

$$I_{lim\ Comp}(T) = \frac{V_{comp} + V_{out} \cdot \left(\frac{R_{1P2}}{R_{1P2} + R14} \cdot \frac{RKTY(T)}{RKTY(T) + R13} - \frac{R_{1P2}}{R_{1P2} + R15} \right)}{\frac{R2}{R2 + R1} \cdot \frac{R14}{R14 + R_{1P2}} \cdot RL(T)}$$

- 20 The curves of figure 6 show the variations in the limiting current Ilim as a function of temperature T in degrees, the curve L1 showing this variation in the case of a system not equipped with the device of the invention, curve L2 in the case of a system equipped with the measurement device according to the invention. These curves reveal the effectiveness of the
25 measurement device according to the invention, the variation in limiting current with the temperature-compensated device according to the invention being almost zero.

ABSTRACT

~~DEVICE FOR NON-DISSIPATIVE MEASUREMENT OF THE CURRENT IN AN INDUCTOR~~

The invention relates to a device for measuring current in an inductor 12 , which device is intended to be connected in parallel with said inductor, comprising two terminals A and B. The device comprises: 10 a network in parallel with the inductor and connected to the terminals A and B having a resistor $R2$ in series with a resistor $R1$ in parallel with a capacitor $C1$; 16 a voltage offset circuit having a DC voltage generator E connected in parallel with an offset resistor R_{offset} in series with two resistors in parallel $R3$ and $R4$, the positive pole of this voltage source being connected to terminal B of the inductor; 20 a temperature compensation circuit comprising a current source controlled as a function of the temperature, one of the two terminals of the current source being connected to the negative pole of the generator E, the other terminal of the current source being connected to different points of the measurement device according to the direction of variation of the current of the source as a function of the temperature. The measurement of voltage V_{mes} , the image of the current I in the inductor 12 , is performed between the common point between the resistors $R1$, $R2$ of the network and the common point between the offset resistor and the two resistors $R3$ and $R4$.

~~Application: measurements of currents in the inductors of switched mode power supplies, voltage step-up, step-down converters.~~

~~Figure 4a~~